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What is claimed is:

1. A method for avoiding data loss in a PDA, wherein the PDA has a RAM to store user information, a battery to power the PDA, a CPU, and a nonvolatile accessible memory with a predetermined region, the method comprising:

backing up user information stored in the RAM into the predetermined region when remaining power of the battery is lower than a default value; and

restoring user information from the predetermined region to the RAM when system power is recovered.

2. The method as claimed in claim 1, wherein the PDA further comprises a power detection unit to output an enable signal when remaining power of the battery is detected as lower than the default value, and output a recovery signal when remaining power of the battery exceeds the default value, wherein the CPU backs up user information stored in the RAM to the predetermined region in response to the enable signal, and restores user information from the predetermined region to the RAM in response to the recovery signal.

3. The method device as claimed in claim 1, further comprising a step of interrupting access to the RAM before backing up user information into the predetermined region.

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4. The method as claimed in claim 1, wherein user information comprises file system, registry, and global operating system settings.

5. A method for avoiding data loss in a PDA, wherein the PDA has a RAM to store user information, a battery to power the PDA, a CPU, a nonvolatile accessible memory to store preset data, and a user interface to output an enable signal and a recovery signal, wherein the nonvolatile accessible memory has a predetermined region, the method comprising:

backing up user information stored in the RAM into the predetermined region in response to the enable signal; and

restoring user information from the predetermined region to the RAM in response to the recovery signal.

6. The method as claimed in claim 5, further comprising a step of interrupting access to the RAM before backing up user information into the predetermined region.

7. The method as claimed in Claim 6, wherein user information comprises file system, registry, and global operating system settings.

8. A PDA capable of avoiding data loss, comprising:

a nonvolatile accessible memory having a predetermined region;

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a RAM to store user information;

a battery to power the PDA; and

a power detection unit to output an enable signal when remaining power of the battery is detected as lower than the default value, and to output a recovery signal when remaining power of the battery exceeds the default value.

9. The PDA as claimed in claim 8, wherein the CPU interrupts access to the RAM before backing up user information into the predetermined region.

10. The PDA as claimed in claim 8, wherein the predetermined region backs up user information only.

11. The PDA as claimed in claim 8, further comprising a user interface to output the enable single and the recovery signal.

12. The PDA as claimed in claim 8, wherein the nonvolatile accessible memory further stores preset data.

13. The PDA as claimed in claim 8, wherein the nonvolatile accessible memory is flash memory.

14. The PDA as claimed in claim 8, wherein the RAM is SDRAM.

15. The PDA as claimed in claim 5, wherein the power detection unit comprises:

an amplifier having an output terminal coupled to the CPU;

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a first resistor coupled between a positive end of the battery and a non-inversion input terminal of the amplifier;

a second resistor coupled between a negative end of the battery and ground;

a third resistor coupled between a reference voltage and an inversion input terminal of the amplifier;

a fourth resistor coupled between the non-inversion input terminal of the amplifier and the ground; and

a fifth resistor coupled to the output terminal of the amplifier.

16. A PDA capable of avoiding data loss, comprising:

a nonvolatile accessible memory storing preset data and programs;

a RAM to store user information;

an external flash memory having a predetermined region;

a battery to power the PDA;

a power detection unit to output an enable signal when remaining power of the battery is detected as lower than a default value, and output a recovery signal when remaining power of the battery exceeds the default value; and

a CPU coupled to the nonvolatile accessible memory, the RAM and the external flash memory, to back up user data stored in the RAM to the

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predetermined region when receiving the enable signal and to restore user information to the RAM when receiving the recovery signal.

17. The PDA as claimed in claim 16, wherein the CPU interrupts access to RAM before backing up user information into the predetermined region.

18. The PDA as claimed in claim 16, wherein the predetermined region of the external flash memory backs up user information only.

19. The PDA as claimed in claim 16, further comprising a user interface to output the enable signal and the recovery signal.

20. The PDA as claimed in claim 16, wherein the RAM is synchronous DRAM (SDRAM).

21. A method for avoiding data loss in a PDA, wherein the PDA has a RAM to store user information, a battery to power the PDA, and a nonvolatile accessible memory to store preset data, and the nonvolatile accessible memory has a predetermined region, the method comprising:

backing up user information stored in the RAM to the predetermined region in response to voltage variation of the battery.